Plan 3 February 20 – February 27

- Read Sections 4.1-12 of Chapter 4.
- Download Logisim via the course web page. It is useful for solving the following exercises.
- Exercise 4. Draw the logical diagrams for the parity generator and the parity checker described on pages 142-143 in the textbook. Use as few gates as possible.
- Exercise 5. Solve exercise 25 in Chapter 3 (page 173).
- Exercise 6. Solve the exercise on the next page.

Exercise 6

Binary-Coded Decimal (BCD) is a representation of the decimal digits 0 to 9, using their binary coded equivalent. In this representation, 0 would be 0000, and 9 would be 1001.

A seven-segment display (SSD) is a set of LEDs that displays a numerical digit (common in digital watches, calculators and clock radios). The display has seven outputs (a, b, c, d, e, f, g), as shown below.

$$\begin{array}{c|c}
a \\
f \\
g \\
b \\
c \\
d
\end{array}$$

A BCD-to-SSD decoder converts a set of four inputs (x1, x2, x3, x4) to a seven-segment output (a, b, c, d, e, f, g).

(1) Provide the truth table.

(2) Draw a circuit diagram for a decoder.